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FEE TRANSMITTAL For FY 2007 <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27		Complete if Known		
		Application Number	Patent#: 7,243,290	
		Filing Date	Issued: July 10, 2007	
		First Named Inventor	Keith R. Slavin	
		Examiner Name	E. T. Abraham	
TOTAL AMOUNT OF PAYMENT (\$)		100.00	Art Unit	2133
			Attorney Docket No.	M4065.0710/P710

METHOD OF PAYMENT (check all that apply)				
<input type="checkbox"/> Check	<input checked="" type="checkbox"/> Credit Card	<input type="checkbox"/> Money Order	<input type="checkbox"/> None	<input type="checkbox"/> Other (please identify):
<input checked="" type="checkbox"/> Deposit Account	Deposit Account Number: 04-1073		Deposit Account Name: Dickstein Shapiro LLP	
For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)				
<input type="checkbox"/> Charge fee(s) indicated below		<input type="checkbox"/> Charge fee(s) indicated below, except for the filing fee		
<input type="checkbox"/> Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17		<input checked="" type="checkbox"/> Credit any overpayments		

FEE CALCULATION							
1. BASIC FILING, SEARCH, AND EXAMINATION FEES							
	FILING FEES		SEARCH FEES		EXAMINATION FEES		
		Small Entity		Small Entity		Small Entity	
Application Type	Fee (\$)	Fee (\$)	Fee (\$)	Fee (\$)	Fee (\$)	Fee (\$)	Fees Paid (\$)
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	
2. EXCESS CLAIM FEES							
Fee Description						Fee (\$)	Small Entity Fee (\$)
Each claim over 20 (including Reissues)						50	25
Each independent claim over 3 (including Reissues)						200	100
Multiple dependent claims						360	180
Total Claims		Extra Claims	Fee (\$)	Fee Paid (\$)	Multiple Dependent Claims		
- =		x	=		Fee (\$)	Fee Paid (\$)	
HP = highest number of total claims paid for, if greater than 20.							
Indep. Claims		Extra Claims	Fee (\$)	Fee Paid (\$)			
- =		x	=				
HP = highest number of independent claims paid for, if greater than 3.							
3. APPLICATION SIZE FEE							
If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).							
Total Sheets	Extra Sheets	Number of each additional 50 or fraction thereof	Fee (\$)	Fee Paid (\$)			
- 100 =	/50 =	(round up to a whole number) x	=				
4. OTHER FEE(S)							
Non-English Specification, \$130 fee (no small entity discount)						Fees Paid (\$)	
Other (e.g., late filing surcharge): 1811 Certificate of correction						100.00	

SUBMITTED BY			
Signature		Registration No. (Attorney/Agent)	28,371
Name (Print/Type)	Thomas J. D'Amico	Telephone	(202) 420-2232
		Date	August 16, 2007

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CofC/#

Docket No.: M4065.0710/P710
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Keith R. Slavin

Patent No.: 7,243,290

Issued: July 10, 2007

For: DATA ENCODING FOR FAST CAM AND
TCAM ACCESS TIMES

Certificate
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of Correction

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 C.F.R. §§ 1.322 & 1.323**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

In the Specification, the PTO erroneously failed to indent the shift rows in the tables as indicated in the application. Consequently, the errors in the following tables are to be corrected:

Column 5, lines 45-49:

“1 0 1 1 1 0 1 1 8-bit comparand
1 0 1 1 1 0 1 1 obtained by shifting the above word right 1 bit
1 1 1 0 0 1 1 0 1 9-bit column-wise "XOR-WOR" result when 2 bits are in
column”

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DSMDB-2303489v01

Should read --

1 0 1 1 1 0 1 1 8-bit comparand

1 0 1 1 1 0 1 1 obtained by shifting the above word right 1 bit

1 1 1 0 0 1 1 0 1 9-bit column-wise "XOR-WOR" result when 2 bits are in column--;

Column 6, lines 15-20:

"1 0 1 1 1 0 1 1 1 1

1 0 1 1 1 0 1 1 1 1 shift upper word right 1 bit to get lower word

1 1 1 0 0 1 1 0 0 0 1 11-bit column-wise "XOR-WOR" result when 2 bits in column"

Should read --

1 0 1 1 1 0 1 1 1 1

1 0 1 1 1 0 1 1 1 1 shift upper word right 1 bit to get lower word

1 1 1 0 0 1 1 0 0 0 1 11-bit column-wise "XOR-WOR" result when 2 bits in column--;

Column 8, lines 62-66:

"1 0 1 1 1 0 1 1 (1) 8-bit incoming CAM word

1 0 1 1 1 0 1 1 (2) obtained by shifting above word right 1 bit

1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit encoded CAM word: column-wise XOR-WOR of (1) and (2), 0-padded"

Should read --

1 0 1 1 1 0 1 1 (1) 8-bit incoming CAM word

1 0 1 1 1 0 1 1 (2) obtained by shifting above word right 1 bit

1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit encoded CAM word: column-wise XOR-WOR of (1) and (2), 0-padded--;

Column 9, lines 14-21:

“1 0 1 1 1 0 1 1 1 1 0 0 (4) 12 bit comparand, top 8 prefix matches (1)
 1 1 1 1 1 1 1 1 0 0 0 0 (5) "AND" mask for 8-bit prefix (top 8 bits enabled)
 1 0 1 1 1 0 1 1 0 0 0 0 (6) column-wise "AND" of (4) and (5)
 1 0 1 1 1 0 1 1 0 0 0 0 (7) is (6) shifted right by 1 bit
 1 1 1 0 0 1 1 0 1 0 0 0 0 (8) column-wise "XOR-WOR" of (6) and (7)
 1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit match word in CAM storage location”

Should read --

1 0 1 1 1 0 1 1 1 1 0 0 (4) 12 bit comparand, top 8 prefix matches (1)
 1 1 1 1 1 1 1 1 0 0 0 0 (5) "AND" mask for 8-bit prefix (top 8 bits enabled)
 1 0 1 1 1 0 1 1 0 0 0 0 (6) column-wise "AND" of (4) and (5)
 1 0 1 1 1 0 1 1 0 0 0 0 (7) is (6) shifted right by 1 bit
 1 1 1 0 0 1 1 0 1 0 0 0 0 (8) column-wise "XOR-WOR" of (6) and (7)
 1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit match word in CAM storage location--;

Column 9, lines 37-45:

“1 0 1 1 0 0 1 1 1 1 0 0 (9) 12 bit comparand
 1 1 1 1 1 1 1 1 0 0 0 0 (10) "AND" mask for 8-bit prefix (top 8 bits enabled)
 1 0 1 1 0 0 1 1 0 0 0 0 (11) column-wise "AND" of (9) and (10)
 1 0 1 1 0 0 1 1 0 0 0 0 (12) is (11) shifted right by 1 bit
 1 1 1 0 1 0 1 0 1 0 0 0 0 (13) column-wise "XOR-WOR" of (11) and (12)
 1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit encoded CAM word stored in a CAM storage
 location
 0 0 0 0 1 1 0 0 0 0 0 0 0 (14) mismatched bits between (13) and (3)”

Should read --

1 0 1 1 0 0 1 1 1 1 0 0 (9) 12 bit comparand
 1 1 1 1 1 1 1 1 0 0 0 0 (10) "AND" mask for 8-bit prefix (top 8 bits enabled)
 1 0 1 1 0 0 1 1 0 0 0 0 (11) column-wise "AND" of (9) and (10)
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 1 1 1 0 0 1 1 0 1 0 0 0 0 (3) 13-bit encoded CAM word stored in a CAM storage
 location
 0 0 0 0 1 1 0 0 0 0 0 0 0 (14) mismatched bits between (13) and (3)--;

Column 9, lines 59-64:

“1 0 1 1 1 0 1 1 (1) 8-bit incoming TCAM word
 1 0 1 1 1 0 1 1 (2) obtained by shifting above word right 1 bit
 1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word: column-wise "XOR-
 WOR" of (1) and (2), X padded”

Should read --

1 0 1 1 1 0 1 1 (1) 8-bit incoming TCAM word
 1 0 1 1 1 0 1 1 (2) obtained by shifting above word right 1 bit
 1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word: column-wise "XOR-
 WOR" of (1) and (2), X padded--;

Column 10, lines 8-15:

“1 0 1 1 1 0 1 1 1 1 0 0 (4) 12 bit comparand, top 8 prefix matches (1)
 1 1 1 1 1 1 1 1 0 1 1 1 (5) single bit "AND" mask for 8 bit prefix
 1 0 1 1 1 0 1 1 0 1 0 0 (6) column-wise "AND" of (4) and (5)
 1 0 1 1 1 0 1 1 0 1 0 0 (7) is (6) shifted right by 1 bit

1 1 1 0 0 1 1 0 1 1 1 0 0 (8) column-wise "XOR-WOR" of (6) and
(7)

1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)"

Should read --

1 0 1 1 1 0 1 1 1 1 0 0 (4) 12 bit comparand, top 8 prefix matches (1)
1 1 1 1 1 1 1 1 0 1 1 1 (5) single bit "AND" mask for 8 bit prefix
1 0 1 1 1 0 1 1 0 1 0 0 (6) column-wise "AND" of (4) and (5)
1 0 1 1 1 0 1 1 0 1 0 0 (7) is (6) shifted right by 1 bit
1 1 1 0 0 1 1 0 1 1 1 0 0 (8) column-wise "XOR-WOR" of (6) and
(7)
1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)--; and

Column 10, lines 27-35:

"1 0 1 1 0 0 1 1 1 1 0 0 (9) 12 bit comparand 1 bit different to (4)
1 1 1 1 1 1 1 1 0 1 1 1 (10) single bit "AND" mask for 8-bit prefix
1 0 1 1 0 0 1 1 0 1 0 0 (11) column-wise "AND" of (9) and (10)
1 0 1 1 0 0 1 1 0 1 0 0 (12) is (11) shifted right by 1 bit
1 1 1 0 1 0 1 0 1 1 1 0 0 (13) column-wise "XOR-WOR" of (11) and (12)
1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)
0 0 0 0 1 1 0 0 0 0 0 0 0 (14) mismatched bits between (13) and (3)"

Should read --

1 0 1 1 0 0 1 1 1 1 0 0 (9) 12 bit comparand 1 bit different to (4)
1 1 1 1 1 1 1 1 0 1 1 1 (10) single bit "AND" mask for 8-bit prefix
1 0 1 1 0 0 1 1 0 1 0 0 (11) column-wise "AND" of (9) and (10)
1 0 1 1 0 0 1 1 0 1 0 0 (12) is (11) shifted right by 1 bit

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1 1 1 0 1 0 1 0 1 1 1 0 0 (13) column-wise "XOR-WOR" of (11) and (12)

1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)

0 0 0 0 1 1 0 0 0 0 0 0 0 (14) mismatched bits between (13) and (3)--.

Also in the Specification, Applicant made the following errors to be corrected:

Column 1, line 62, "it" should read --its--;

Column 5, line 36, "used then" should read --used, then--;

Column 12, lines 47-48, "an miscellaneous" should read --a miscellaneous--;

Column 12, line 49, "an legacy" should read --a legacy--;

Column 12, line 50, "also coupled" should read --also be coupled--;

Column 12, line 60, "an local" should read --a local--;

Column 12, line 64, "an universal" should read --a universal--;

Column 12, line 65, "via to the" should read --via the--; and

Column 13, line 1, "to one additional" should read --to additional--.

In the Claims, Applicant made the following errors to be corrected:

Claim 15, column 14, line 54, "hierarchical of" should read --hierarchical--; and

Claim 30, column 16, line 24, "encode" should read --encoded--.

Some of the errors were found in the application as filed by Applicant. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 C.F.R. § 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0710/P710.

Dated: August 16, 2007

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Michael A. Weinstein

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AUG 20 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 4

PATENT NO. : 7,243,290
APPLICATION NO. : 10/616,958
ISSUE DATE : July 10, 2007
INVENTOR : Keith R. Slavin

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, the following errors are corrected:

Column 1, line 62, "it" should read --its--;

Column 5, line 36, "used then" should read --used, then--;

Column 5, lines 45-49:

"1 0 1 1 1 0 1 1 8-bit comparand

1 0 1 1 1 0 1 1 obtained by shifting the above word right 1 bit

1 1 1 0 0 1 1 0 1 9-bit column-wise "XOR-WOR" result when 2 bits are in column"

Should read --

1 0 1 1 1 0 1 1 8-bit comparand

1 0 1 1 1 0 1 1 obtained by shifting the above word right 1 bit

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Column 6, lines 15-20:

"1 0 1 1 1 0 1 1 1 1

1 0 1 1 1 0 1 1 1 1 shift upper word right 1 bit to get lower word

1 1 1 0 0 1 1 0 0 0 1 11-bit column-wise "XOR-WOR" result when 2 bits in column"

Should read --

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1 0 1 1 1 0 1 1 1 1	
1 0 1 1 1 0 1 1 1 1	shift upper word right 1 bit to get lower word
1 1 1 0 0 1 1 0 0 0 1	11-bit column-wise "XOR-WOR" result when 2 bits in column--;
Column 8, lines 62-66:	
"1 0 1 1 1 0 1 1	(1) 8-bit incoming CAM word
1 0 1 1 1 0 1 1	(2) obtained by shifting above word right 1 bit
1 1 1 0 0 1 1 0 1 0 0 0 0	(3) 13-bit encoded CAM word: column-wise XOR-WOR of (1) and (2), 0-padded"
Should read --	
1 0 1 1 1 0 1 1	(1) 8-bit incoming CAM word
1 0 1 1 1 0 1 1	(2) obtained by shifting above word right 1 bit
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Column 9, lines 14-21:	
"1 0 1 1 1 0 1 1 1 1 0 0	(4) 12 bit comparand, top 8 prefix matches (1)
1 1 1 1 1 1 1 1 0 0 0 0	(5) "AND" mask for 8-bit prefix (top 8 bits enabled)
1 0 1 1 1 0 1 1 0 0 0 0	(6) column-wise "AND" of (4) and (5)
1 0 1 1 1 0 1 1 0 0 0 0	(7) is (6) shifted right by 1 bit
1 1 1 0 0 1 1 0 1 0 0 0 0	(8) column-wise "XOR-WOR" of (6) and (7)
1 1 1 0 0 1 1 0 1 0 0 0 0	(3) 13-bit match word in CAM storage location"
Should read --	
1 0 1 1 1 0 1 1 1 1 0 0	(4) 12 bit comparand, top 8 prefix matches (1)
1 1 1 1 1 1 1 1 0 0 0 0	(5) "AND" mask for 8-bit prefix (top 8 bits enabled)
1 0 1 1 1 0 1 1 0 0 0 0	(6) column-wise "AND" of (4) and (5)
1 0 1 1 1 0 1 1 0 0 0 0	(7) is (6) shifted right by 1 bit
1 1 1 0 0 1 1 0 1 0 0 0 0	(8) column-wise "XOR-WOR" of (6) and (7)
1 1 1 0 0 1 1 0 1 0 0 0 0	(3) 13-bit match word in CAM storage location--;
Column 9, lines 37-45:	
"1 0 1 1 0 0 1 1 1 1 0 0	(9) 12 bit comparand
1 1 1 1 1 1 1 1 0 0 0 0	(10) "AND" mask for 8-bit prefix (top 8 bits enabled)
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1 1 1 0 1 0 1 0 1 0 0 0 0	(13) column-wise "XOR-WOR" of (11) and (12)

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1 1 1 0 0 1 1 0 1 0 0 0 0	(3) 13-bit encoded CAM word stored in a CAM storage location
0 0 0 0 1 1 0 0 0 0 0 0 0	(14) mismatched bits between (13) and (3)"
Should read --	
1 0 1 1 0 0 1 1 1 1 0 0	(9) 12 bit comparand
1 1 1 1 1 1 1 1 0 0 0 0	(10) "AND" mask for 8-bit prefix (top 8 bits enabled)
1 0 1 1 0 0 1 1 0 0 0 0	(11) column-wise "AND" of (9) and (10)
1 0 1 1 0 0 1 1 0 0 0 0	(12) is (11) shifted right by 1 bit
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Should read --	
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Column 10, lines 8-15:	
"1 0 1 1 1 0 1 1 1 1 0 0	(4) 12 bit comparand, top 8 prefix matches (1)
1 1 1 1 1 1 1 1 0 1 1 1	(5) single bit "AND" mask for 8 bit prefix
1 0 1 1 1 0 1 1 0 1 0 0	(6) column-wise "AND" of (4) and (5)
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1 0 1 1 1 0 1 1 0 1 0 0	(6) column-wise "AND" of (4) and (5)
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1 1 1 0 0 1 1 0 1 1 1 0 0	(8) column-wise "XOR-WOR" of (6) and (7)

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1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)--; and

Column 10, lines 27-35:

"1 0 1 1 0 0 1 1 1 1 0 0 (9) 12 bit comparand 1 bit different to (4)
 1 1 1 1 1 1 1 1 0 1 1 1 (10) single bit "AND" mask for 8-bit prefix
 1 0 1 1 0 0 1 1 0 1 0 0 (11) column-wise "AND" of (9) and (10)
 1 0 1 1 0 0 1 1 0 1 0 0 (12) is (11) shifted right by 1 bit
 1 1 1 0 1 0 1 0 1 1 1 0 0 (13) column-wise "XOR-WOR" of (11) and (12)
 1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)
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Should read --

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 1 1 1 0 0 1 1 0 1 X X X X (3) 13-bit encoded TCAM word (padded)
 0 0 0 0 1 1 0 0 0 0 0 0 0 (14) mismatched bits between (13) and (3)--.

Column 12, lines 47-48, "an miscellaneous" should read --a miscellaneous--;

Column 12, line 49, "an legacy" should read --a legacy--;

Column 12, line 50, "also coupled" should read --also be coupled--;

Column 12, line 60, "an local" should read --a local--;

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Column 12, line 65, "via to the" should read --via the--; and

Column 13, line 1, "to one additional" should read --to additional--.

In the Claims, the following errors are corrected:

Claim 15, column 14, line 54, "hierarchical of" should read --hierarchical--; and

Claim 30, column 16, line 24, "encode" should read --encoded--.

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